

Media Alert



Interest in IP-XACT Specification Continues to Grow

-- Membership in The SPIRIT Consortium Hits Milestone --

MUNICH (Design, Automation & Test Exhibition), Mar. 10, 2008 – Eleven new members have expressed interest in laying the foundation for design flow integration by becoming either Reviewing or Associate members of The SPIRIT Consortium. This brings the organization's roster to more than 100 members. The SPIRIT Consortium is a non-profit entity, comprised of worldwide EDA, IP, system integration, and semiconductor companies dedicated to the adoption of a unified set of specifications for configuring, integrating and verifying IP in advanced SoC design tool sets.

The new Reviewing members include Agnisis Technology, Chip Estimate, Consorzio Pisa Recerche, Curtiss-Wright/Dy4 Inc., EMA Design Automation, and Perfectus Technology.

The SPIRIT Consortium has four membership levels. The Board of Directors develops and approves the IP-XACT roadmap. The Contributing committee is comprised of members who dedicate engineering resources to technical working groups. Reviewing members help validate Alpha and Beta versions of the specification, and Associate members include non-profit organizations and research institutes.

By ensuring that leading academic and non-profit research organizations have access to early-release specifications, The Consortium is increasing its investment in innovation. This ensures that the organization's proposed standard, IP-XACT™ will be used as the basis for future design-flow integration as well as meeting the needs of industry today. New Associate members are ECSI, FPGA Central, IMEC, Silicon Integration Initiative (Si2), and The University of Paderborn.

About The Consortium

The SPIRIT Consortium is a global organization focused on establishing multi-faceted IP/tool integration standards that drive sustainable growth in electronic design. It is comprised of leading EDA, IP, system integration, and semiconductor companies dedicated to the adoption of a unified set of specifications for configuring, integrating, and verifying IP in advanced SoC design tool sets. For more information on The SPIRIT Consortium and its goals, please visit www.spiritconsortium.org.

For more information about The Spirit Consortium, please contact Jayne Scheckla at 503-685-4833 or jayne_scheckla@mentor.com.

###