



The Merger of Accellera & The SPIRIT Consortium

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Accellera + The SPIRIT Consortium

Combination is an industry changing event

- Accellera: World class language based Design & Verification standards
- The SPIRIT Consortium: IP Deployment & Reuse standards.
- Complementary standards for Design, Verification & IP Reuse
- Leverages operational strength of two organizations

Together addressing the electronics industry needs

- standards for design & verification community.
- IP and tool integration standards to improve IP reuse.
- Stronger organization for the next decade.

Broad Industry Support

Board of Directors

Direction set by EDA, IP, Semiconductor and System leaders

Large Membership

Across the industry with many users, EDA vendors and IP suppliers

Accellera + The SPIRIT Consortium Board Members

ARM

Cadence

Denali

Freescale

Intel

LSI

Mentor

Nokia

NXP

SpringSoft

ST Microelectronics

Sun

Synopsys

Texas Instruments

- Accellera Members
- SPIRIT Members
- Common Members

Accellera + The SPIRIT Consortium (Associate + Contributing Members)

Aldec

Azuro

Cisco

Coware

Duolog

IBM

Infineon

Jasper Design

Magillem

MIPS

One Spin

Paradigm Works

Semifore

Silvaco

Sonics

Toshiba

Verilab

Xilinx

XtremeEDA

Accellera:

- Standards Organization for the next Decade
 - proven track record of developing and transferring standards to IEEE
 - Increased focus on IP and Reuse
 - collaborative participation by systems, semiconductors, EDA and IP companies.
 - effective marketing program to reach users
 - recognizes the work done by its technical committee members.

Together we work, together we progress

Chinese Aphorism



Q & A

Background slides

- Organization has developed 10 IEEE standards
- Organization expects approval of three IEEE standards in 2009.
 - 1801 was approved in March 2009
 - P1450 will be approved in June 2009
 - P1800 approval by end of the year 2009

Merger Motivation

- Accellera
- Leverage strength of SPIRIT members and expand into standards for IP reuse
- Additional input & direction for language based standards.
- The SPIRIT Consortium
- Leverage Accellera's strength
 - IEEE Standards process
 - Collaborative participation by a large number of EDA + System + Semiconductor
 - Market awareness and adoption
- Common Goal (transfer to IEEE)
- Strong Infrastructure (TSCs, Legal, Administrator)
- Marketing message efficiency

Accellera

Mission

To drive the worldwide development and use of standards required by systems, semiconductor and design tool companies that enhance a language-based design automation process.

Accellera is an Entity based Standards Consortium
One Company/ One Vote

Accellera's policy is to transfer its standards to the IEEE

www.accellera.org



Accellera Accomplishments

- IEEE 1801 Standard for Design and Verification of Low Power for Integrated Circuits
- IEEE 1076-2008 VHDL
- IEEE 1800 SystemVerilog
- IEEE 1850 Property Specification Language (PSL)
- IEEE 1364 Verilog
- IEEE 1497 Standard Delay Format
- IEEE 1076.4 VITAL (VHDL Initiative Towards ASIC Libraries)
- IEEE 1481 Delay & Power Calculation System
- IEEE 1499 Open Model Interface
- IEEE 1603 Advanced Library Format (ALF)

Accellera Overall Activities

	2003	2004	2005	2006	2007	2008	2009
ITC	1.0	1.1			2.0	2.1	
OVL			1.0	1.6-8	2.0-1	2.2-3	3.0
UCIS					Formation		1.0
V-AMS	2.1	2.2				2.3	IEEE
VIP						Formation	
SV	3.1	3.1a	IEEE	PAR	Merge		1800-2009
UPF				Formation	1.0		1801-2009
PSL	1.01	1.1	IEEE	PAR			1850-2009
VHDL			Formation VHDL 1.0	VHDL 3.0 VHPI 2.4	3.1, IEEE	4.0 1076-2008	
OCI			Formation	1.0	IEEE		1450-2009

The SPIRIT Consortium

Defining standards to solve the problem

- Scope
 - Establish a set of IP and tool integration standards that enable proliferation of IP reuse through design automation
- International standardization organization
 - Formed June 2003, with California non-profit status in 2006
 - Leading multi-vendor design-flow integration today
 - A foundation for future specification development and support
 - Formalizing relationships with other organizations: Eclipse, IEEE, OCP-IP, OSCI, Si2, SPRINT, STARC, W3C, ...
 - All specifications will be pushed in batches through IEEE 1685
- Visit Web site:
 - web site www.spiritconsortium.org

The SPIRIT Consortium Activities

	2003	2004	2005	2006	2007	2008	2009
IP-XACT™	1.0	1.1		1.2 To P1685		1.4	1.5
SystemRDL™				Donation Formation			1.0
IP Tagging						Hosted	