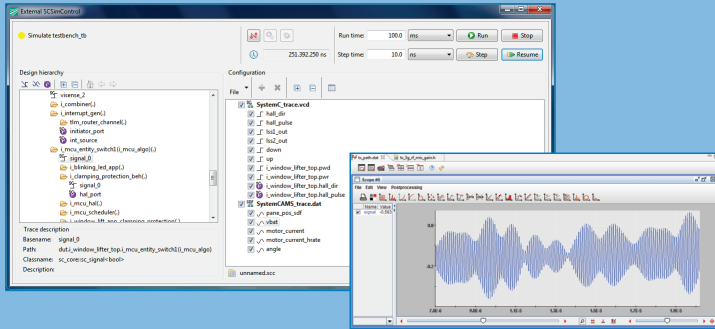
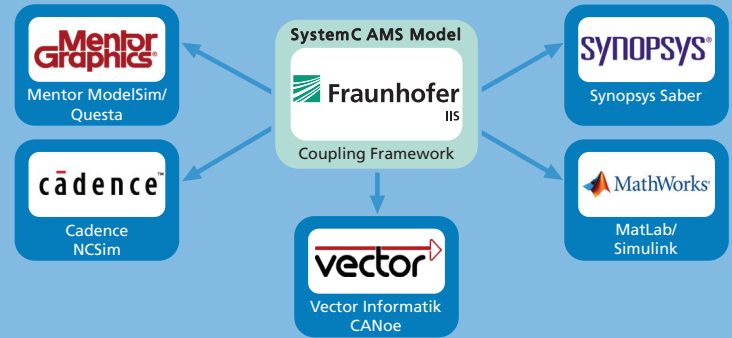


## Simulation Control, Debugging



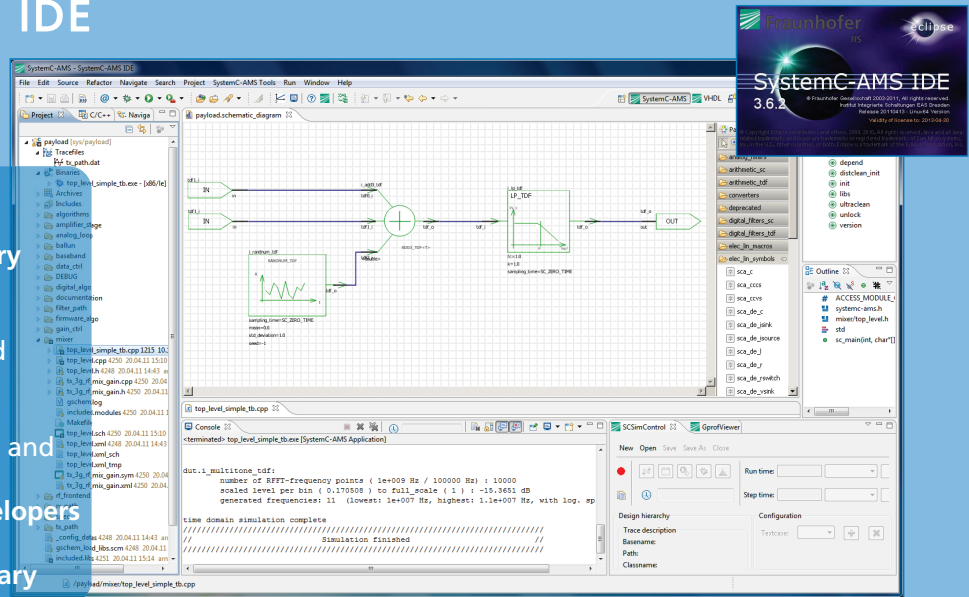
- SCSimCtrl: Advanced Drag'n'Drop Trace Selection, Hierarchy Browsing and Simulation Control
- Debugging Facilities in Stand-alone as well as in Coupled Simulation Scenarios

## Coupling Framework



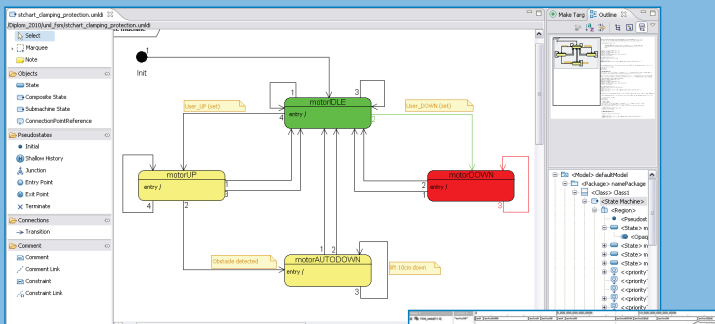
- Coupling SystemC AMS Simulations with Tools from Various EDA Vendors
- IP Protected Model Exchange
- Delivering Customer Models

## SystemC AMS IDE



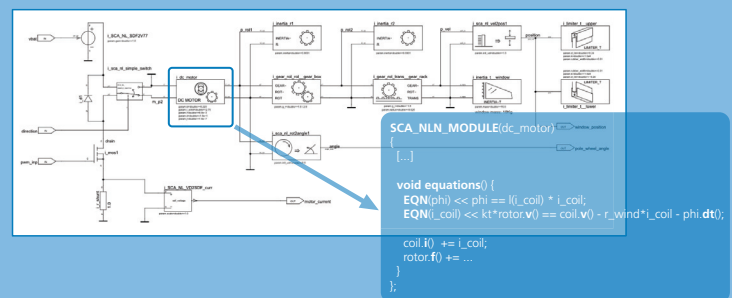
- Powerful Modelling Capabilities
- Schematic Design Entry
- OSCI Compliant
- Full TLM 2.0 Support
- SystemC / VHDL Mixed Language Modelling
- Basic Model Libraries
- Profiling Performance and Memory Leakages
- Designed by the Developers of the SystemC AMS Proof-of-Concept Library

## State Chart Editor



- UML State Chart Syntax
- C/ SystemC Code Generation
- Combined Graphical State and Analogue Tracing

## Extension for Nonlinear Circuits



- Support for Multiple Physical Domains such as electrical, kinematic, thermal
- Several Quantities Predefined, e.g. electrical::charge, kinematic::force
- New Callback Methods, e.g. sca\_module::equations()

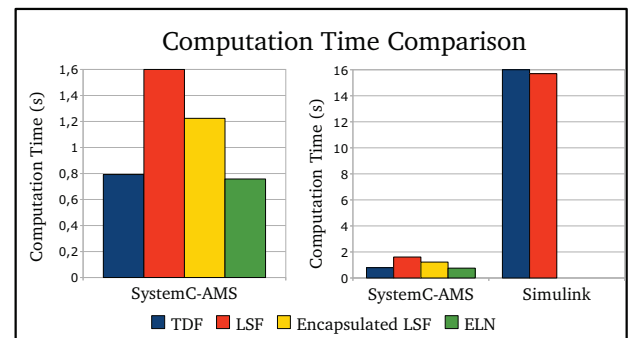
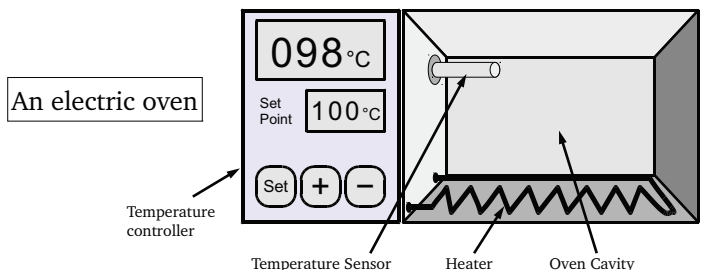
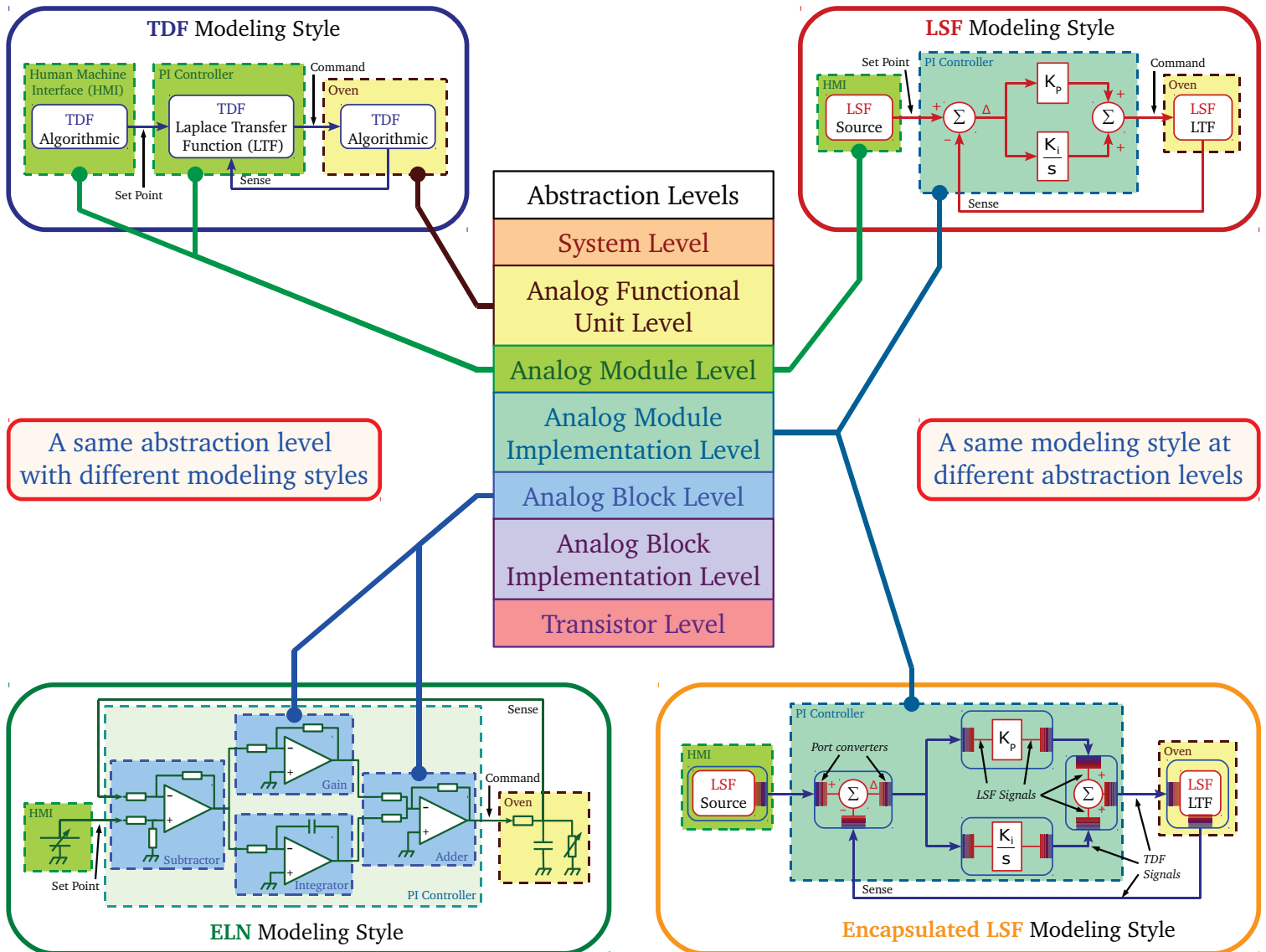
# Analog Design Abstraction Levels and SystemC-AMS Models of Computation

Franck PAUGNAT, Laurent BOUSQUET, Katell MORIN-ALLORY, Laurent FESQUET  
TIMA Laboratory, Grenoble, France

{franck.paugnat, laurent.bousquet, katell.morin-allory, Laurent.Fesquet}@imag.fr

- How to help the analog designer to:
- navigate through the abstraction levels and the modeling styles
  - choose the most suited modeling style to an abstraction level for best accuracy and computational speed

SystemC-AMS Models of Computation (MoC)		
<b>TDF</b> Timed Data Flow	<b>LSF</b> Linear Signal Flow	<b>ELN</b> Electrical Linear Network



# SYCYPHOS - A Framework for Designing Cyber Physical Systems

Florian Schupfer, Josef Wenninger, Christoph Grimm  
Vienna University of Technology, Institute of Computer Technology

Combining distributed computer ('cyber') and physical systems leads to new challenges that are not yet solved, neither by tools nor by methodologies. The methodology handles design issues and provides a (work in progress) tool for the design of cyber physical systems. The tool supports modeling of distributed and heterogeneous systems -- in particular at functional level -- while offering means for power estimation and for analyzing the impact of non-ideal behavior of micro- or nanoelectronic implementations.

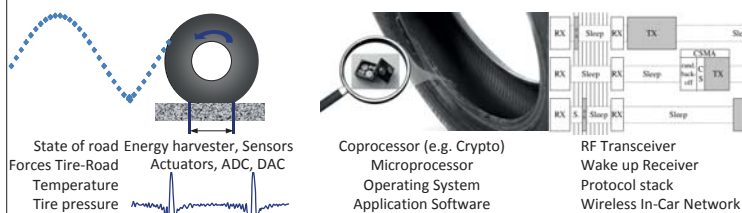
PROBLEM

- No joint simulation of network and Hardware/Software design
- Late verification of properties like current consumption, accuracy or reliability

SYCYPHOS

- Common modeling of network and HW/SW components
- Profiling of accuracy/robustness and power consumption at CPS level

## Example of a Cyber Physical System

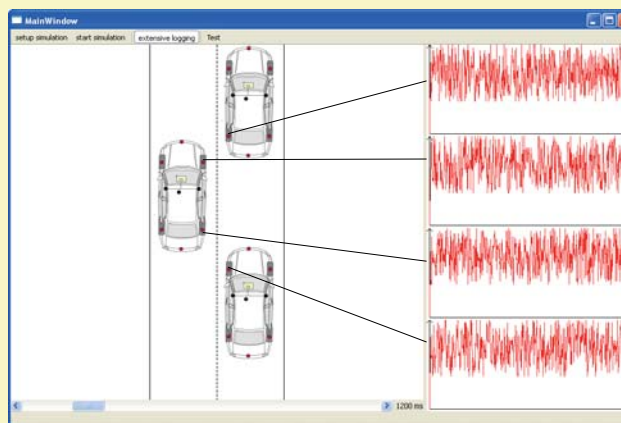
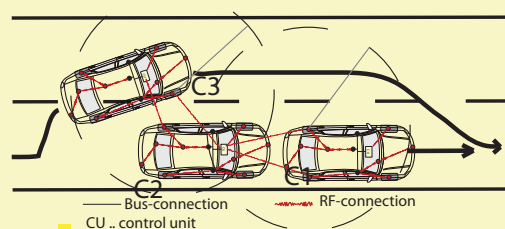


## SYCYPHOS - Features

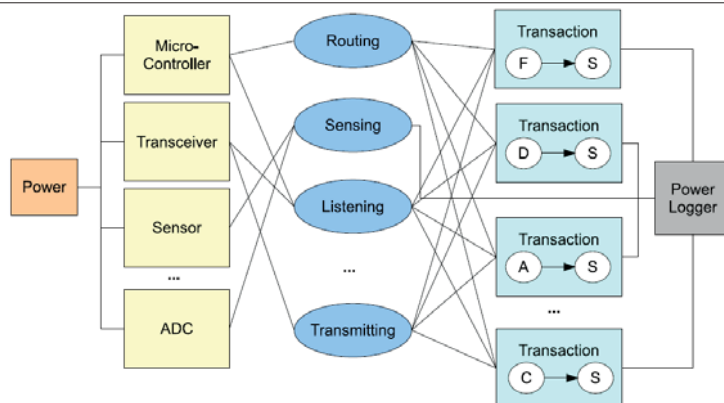
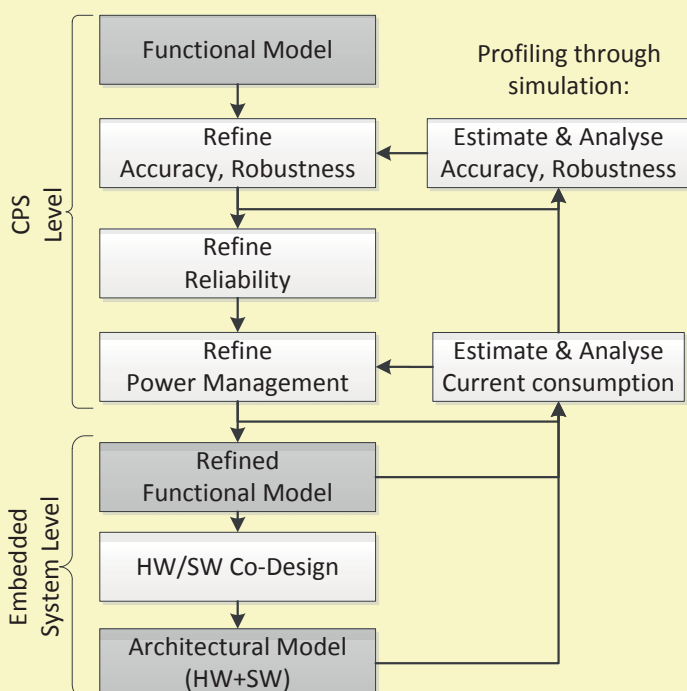
Profiling of:

- Power properties
- Accuracy metrics
- Reliability estimation

## Power Profiling



## Design Methodology



Contact: Prof. Christoph Grimm, Vienna University of Technology  
Institute of Computer Technology: grimm@ict.tuwien.ac.at

This work is conducted as part of the WWTF project MARC which is funded under contract number ICT08\_12.

## SystemC AMS Modeling of a PCR-CE Lab-on-chip For Multithreaded DNA Analysis

Amr Habib, François Pêcheux and Marie-Minerve Louërat

University Pierre et Marie Curie, Computer Science Laboratory LIP6, Paris, France



### Architecture exploration of heterogeneous Applications

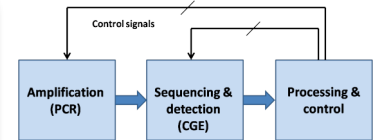
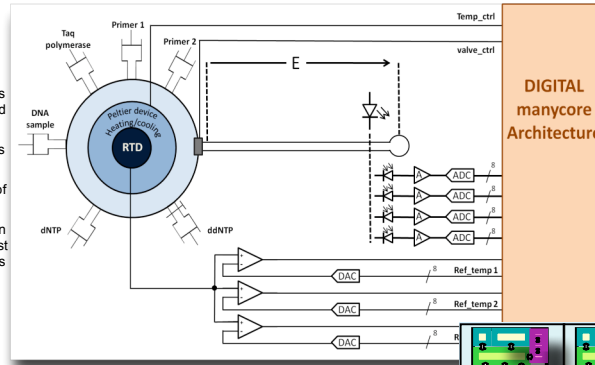
#### Model of a bio-compatible heterogeneous system

Lab-on-chip that encompasses several disciplines such as analog, digital, chemical kinetic reactions, optics and embedded software.

Virtual prototype taking as input an initial DNA concentration as well as the expression of the gene as a DNA string

Able to compare the input string to a huge database of reference samples, and this detects mutation and pathologies.

The simulated model can be used as a simulatable specification at a very high level of abstraction and can be seen as the first refinement step towards the design of a complex heterogeneous and bio-compatible system.

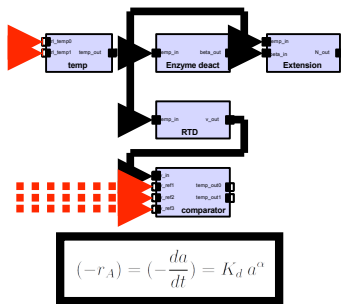


#### Model composed of three parts :

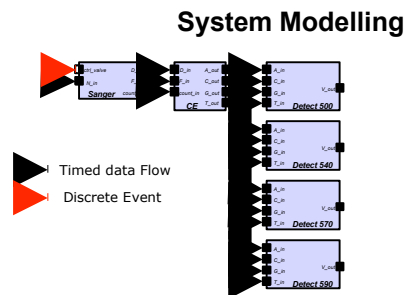
- DNA amplification by **Polymerase Chain Reaction (PCR)**
- Molecular separation by **Capillary Electrophoresis (CE)** and optical detection of fluorescently labeled molecules
- Automated DNA sequencing on a **digital manycore architecture** running a highly multi-threaded **Smith-Waterman** sequencing algorithm.

- Control temperature
- Control cycle duration
- Signal processing
- Information processing
  - Align DNA sequences
  - Compare acquired sequence with detected sequences
  - Search within a database
  - etc ...

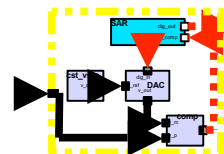
### System Analysis



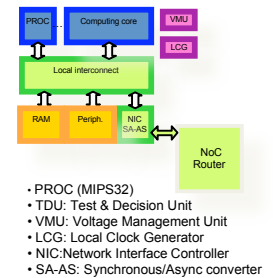
**Polymerase Chain Reaction (PCR)**



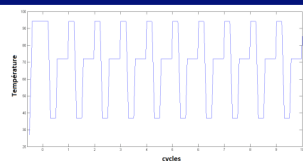
**Capillary Electrophoresis (CE)**



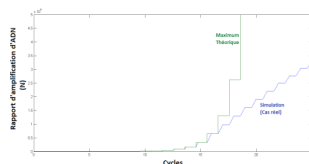
**Optical Detection**



**MP SOC Generic Tile**

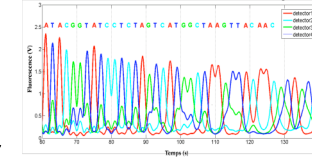


**Thermo-Cycling**

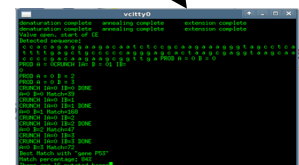
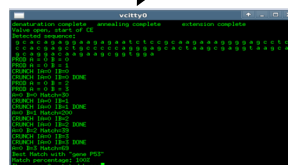


**Amplification Ratio of DNA**

4 minutes to simulate PCR-CE and 100 samples comparison



**Capillary Electrophoresis**



### Simulation results SystemC AMS (Timed Data Flow) and SystemC

# Join the



# Academic Connection Program

**Exchanging knowledge**  
for the introduction of  
the SystemC AMS stan-  
dard into educational  
and teaching programs.

## **Building a network**

- Universities
- Research organizations
- Industry

## **Interested to join this program?**

### **Contact:**

Martin Barnasconi, NXP Semiconductors  
[martin.barnasconi@nxp.com](mailto:martin.barnasconi@nxp.com)

or

Christoph Grimm, Vienna University of Technology  
[grimm@ict.tuwien.ac.at](mailto:grimm@ict.tuwien.ac.at)

**or one of the universities having SystemC AMS already in their curriculum:**



TECHNISCHE  
UNIVERSITÄT  
WIEN  
Vienna University of Technology

Vienna University of Technology (TU Vienna, TU Wien)  
Faculty of Electrical Engineering and Information Technology  
Institute of Computer Technology (ICT)  
Gußhausstraße 27-29/384, 1040 Wien, Austria  
<http://www.ict.tuwien.ac.at>  
Contact person: Christoph Grimm  
[grimm@ict.tuwien.ac.at](mailto:grimm@ict.tuwien.ac.at)



Delft University of Technology (TU Delft)  
Faculty Electrical Engineering, Mathematics and Computer Science (EWI)  
Mekelweg 4, 2628 CD Delft, The Netherlands  
<http://ens.ewi.tudelft.nl/>  
Contact person: Rene van Leuken  
[T.G.R.M.vanLeuken@tudelft.nl](mailto:T.G.R.M.vanLeuken@tudelft.nl)



Université Pierre & Marie Curie (UPMC - Paris 6)  
Laboratoire LIP6-SoC  
4 Place Jussieu, 75005 Paris, France  
<http://www-asim.lip6.fr/>  
Contact person: François Pecheux  
[Francois.Pecheux@lip6.fr](mailto:Francois.Pecheux@lip6.fr)

